

FIG.1

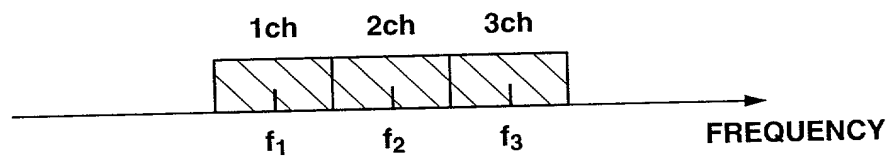


FIG.2

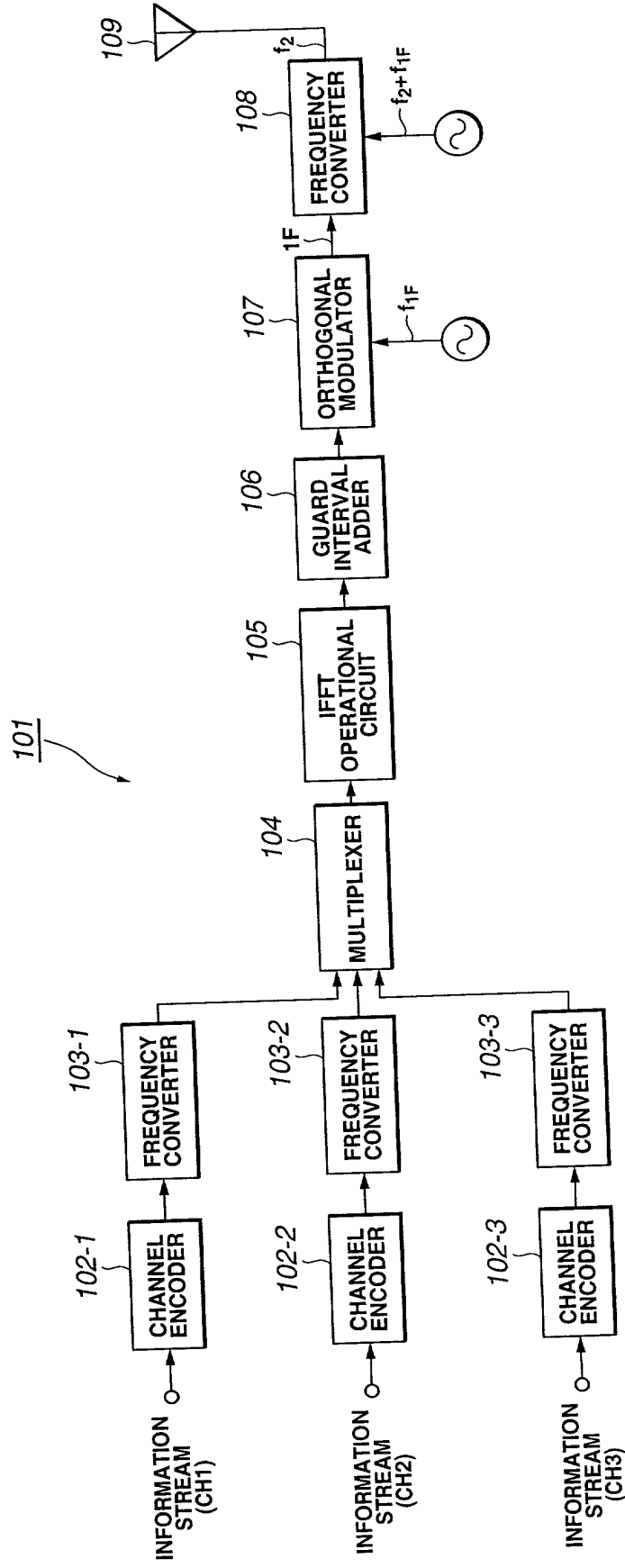


FIG.3

FIG. 4

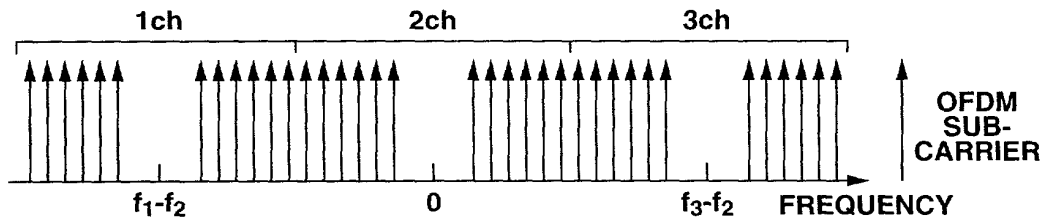


FIG.4

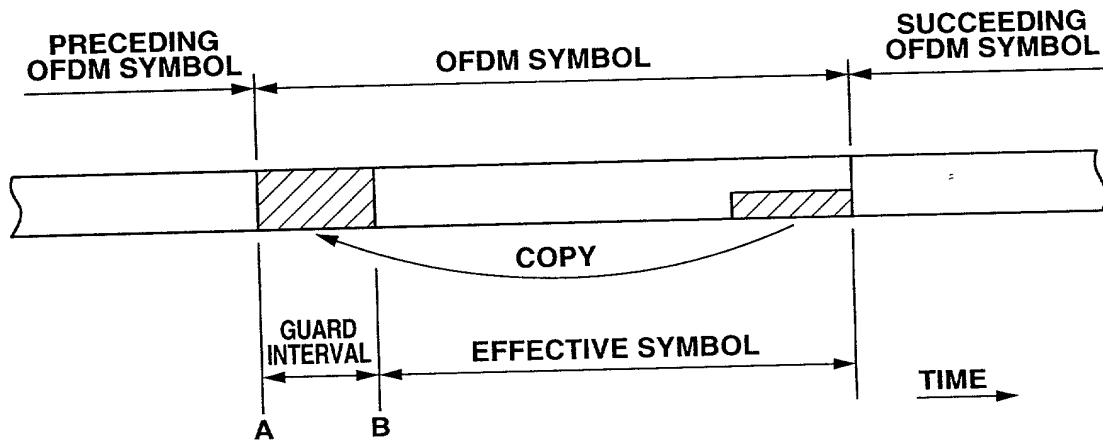


FIG.5

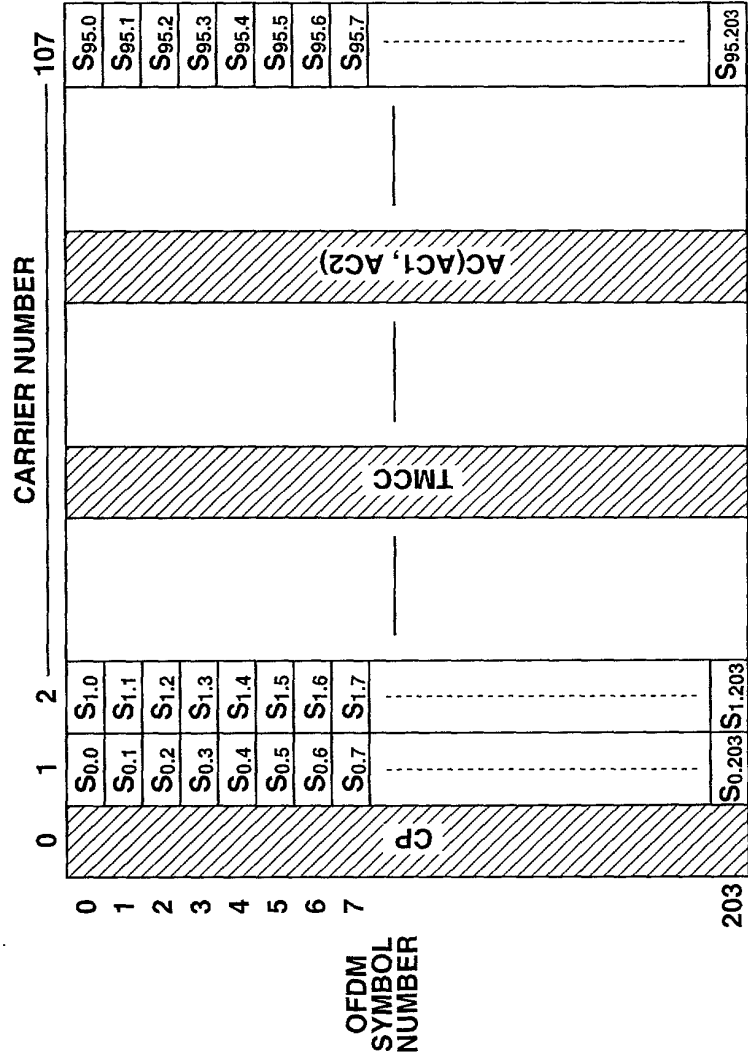


FIG.6

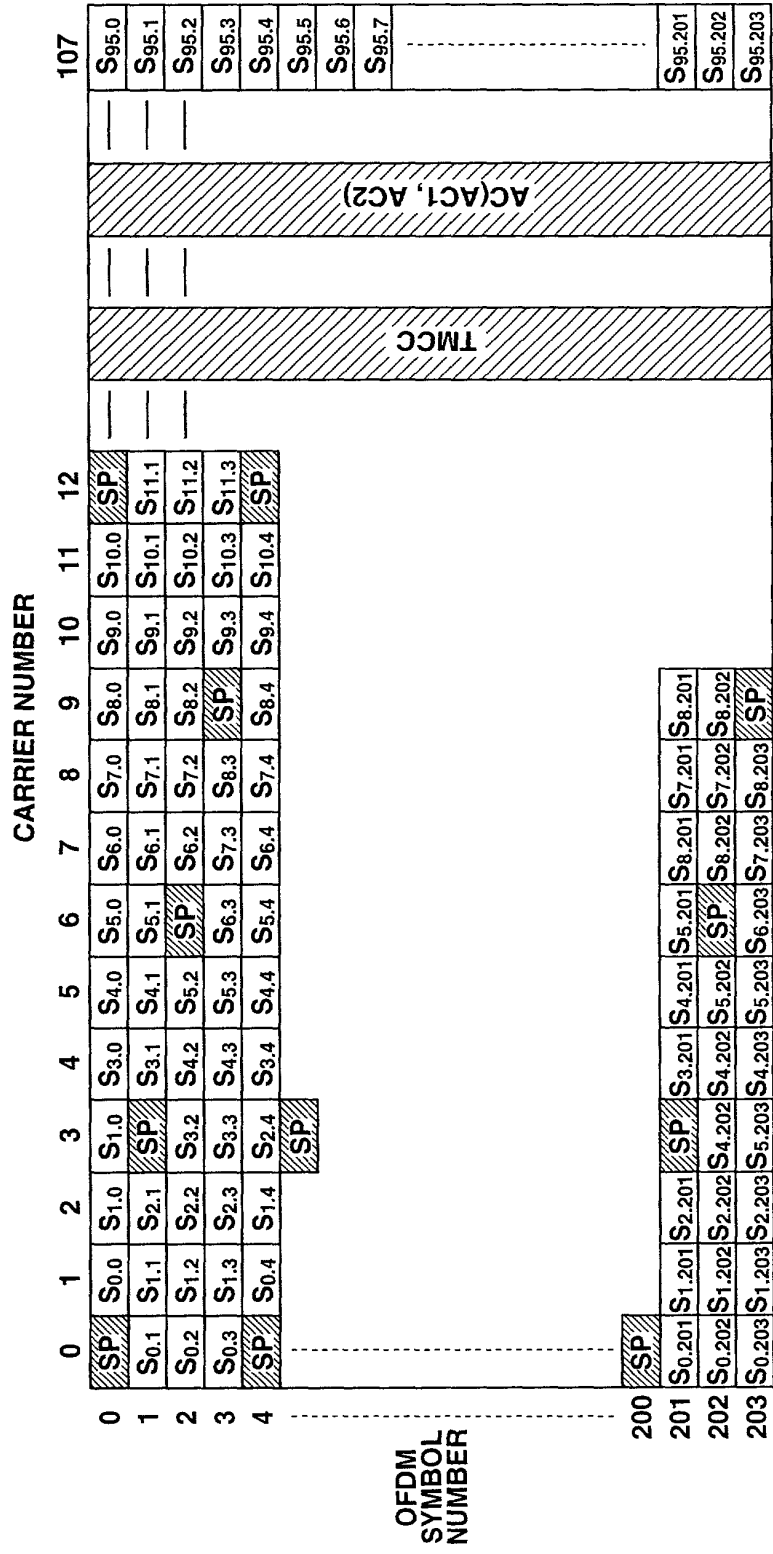


FIG.7

SEGMENT NO.	
CP	0
AC1_1	35
AC1_2	79
AC2_1	3
AC2_2	72
AC2_3	85
AC2_4	89
TMCC 1	49
TMCC 2	61
TMCC 3	96
TMCC 4	99
TMCC 5	104

FIG.8

**CARRIER ARRANGEMENT OF TMCC AND
AC OF SYNCHRONOUS MODULATOR**

SEGMENT NO.	
AC1_1	35
AC1_2	79
TMCC 1	49

FIG.9

B_0	REFERENCE FOR DIFFERENTIAL DEMODULATION
$B_0 \sim B_{16}$	SYNCHRONIZING SIGNAL ($W_0=0011010111101110$, $W_1=1100101000010001$)
$B_{17} \sim B_{19}$	IDENTIFICATION OF SEGMENT FORMAT (DIFFERENTIAL 111, SYNCHRONOUS 000)
$B_{20} \sim B_{121}$	TMCC INFORMATION (102 BITS)
$B_{122} \sim B_{203}$	PARITY BITS

FIG.10

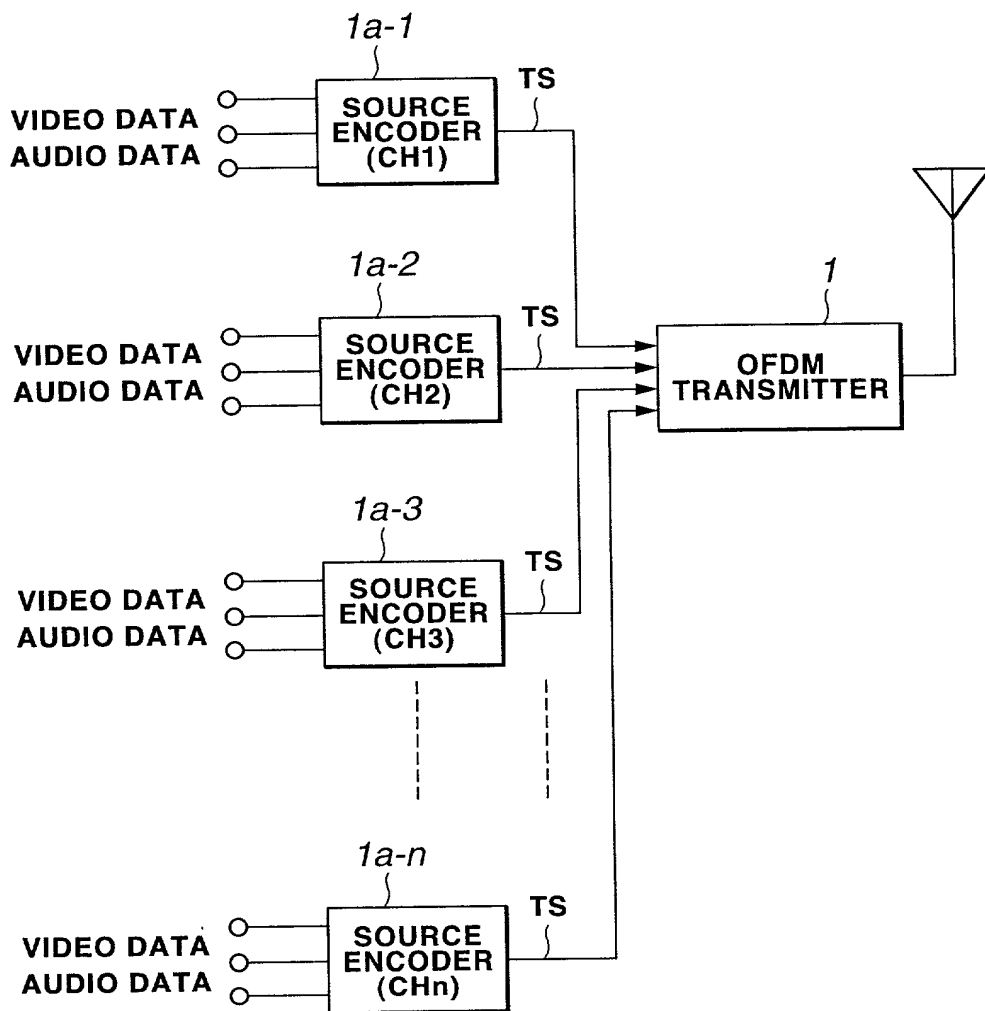


FIG.11

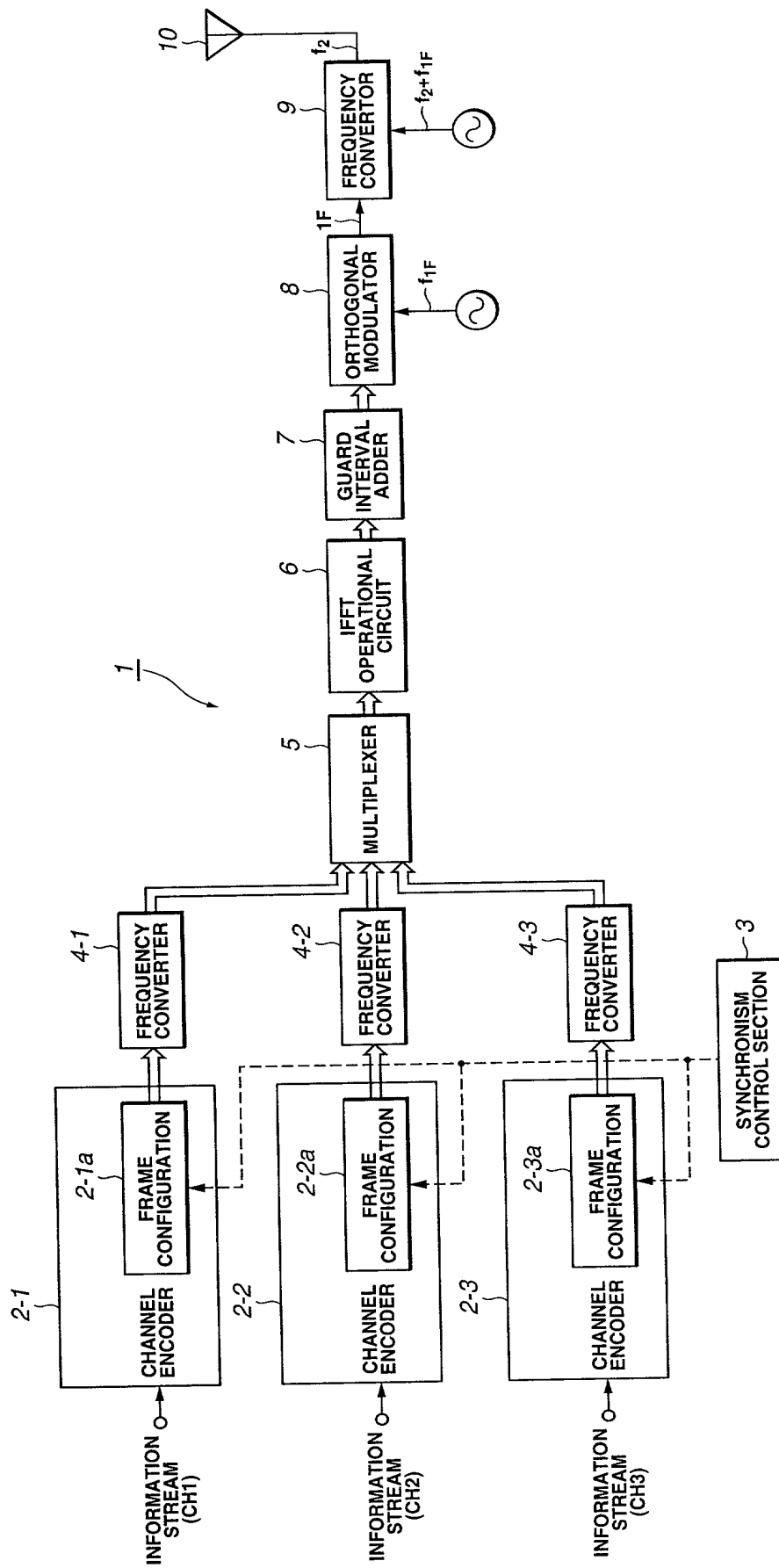


FIG.12

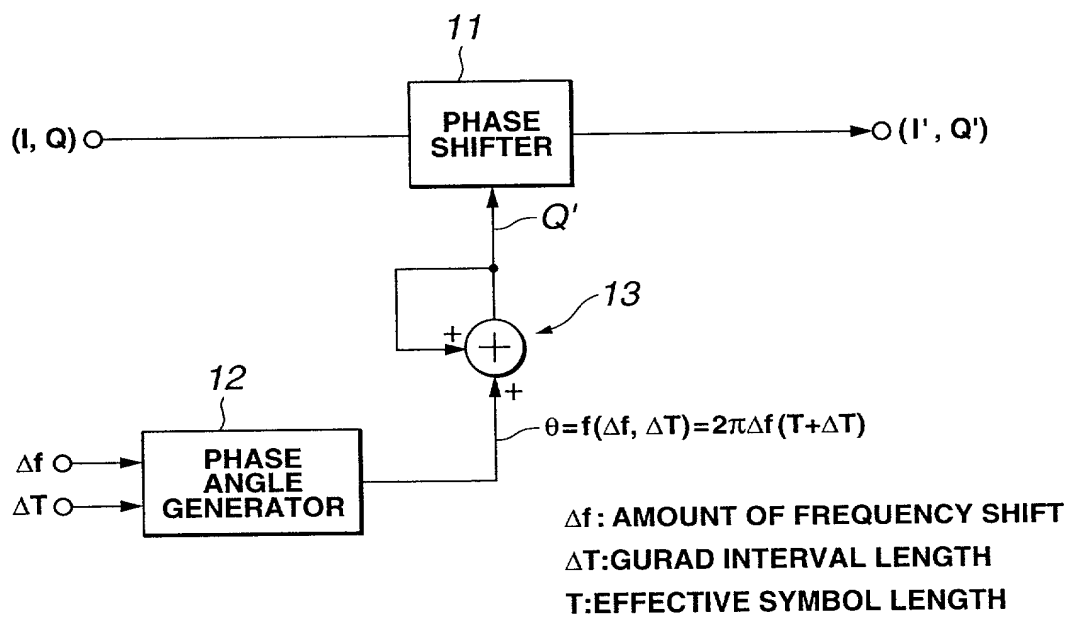


FIG.13

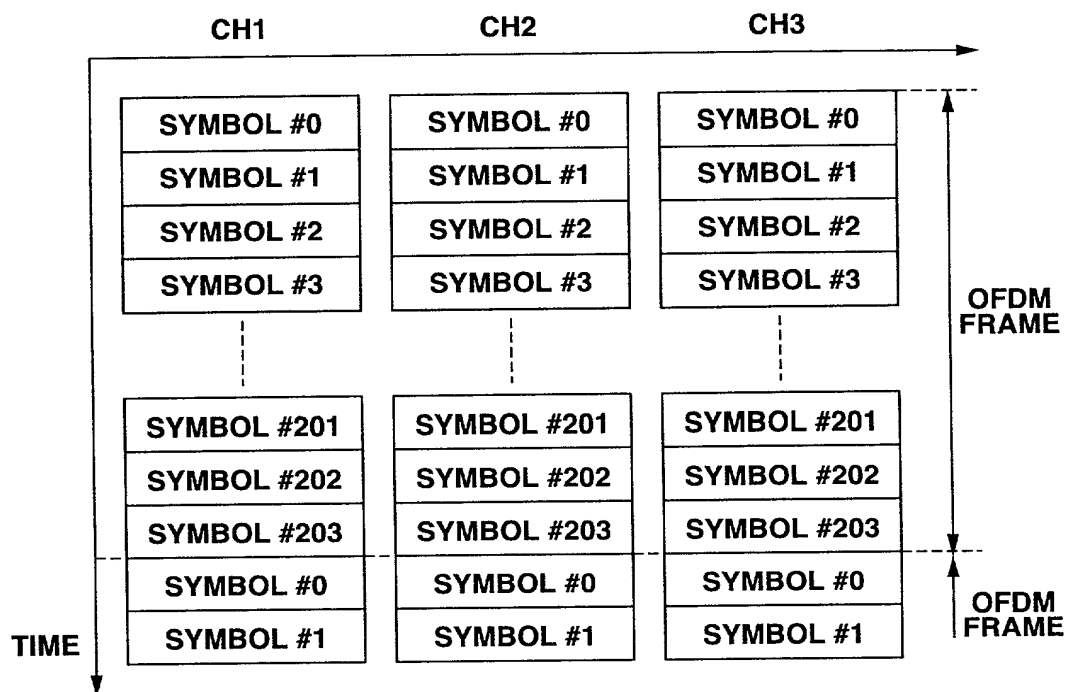


FIG.14

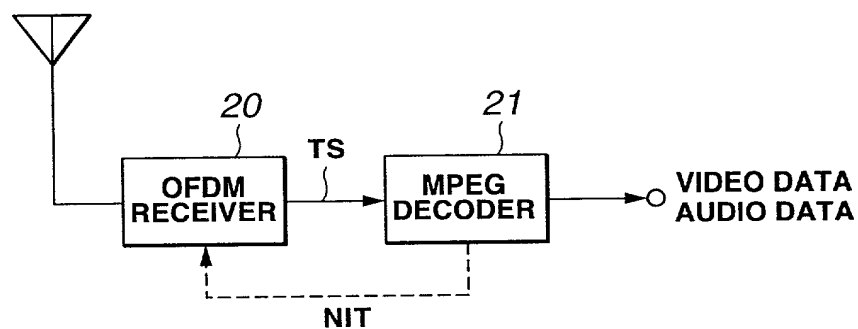


FIG.15

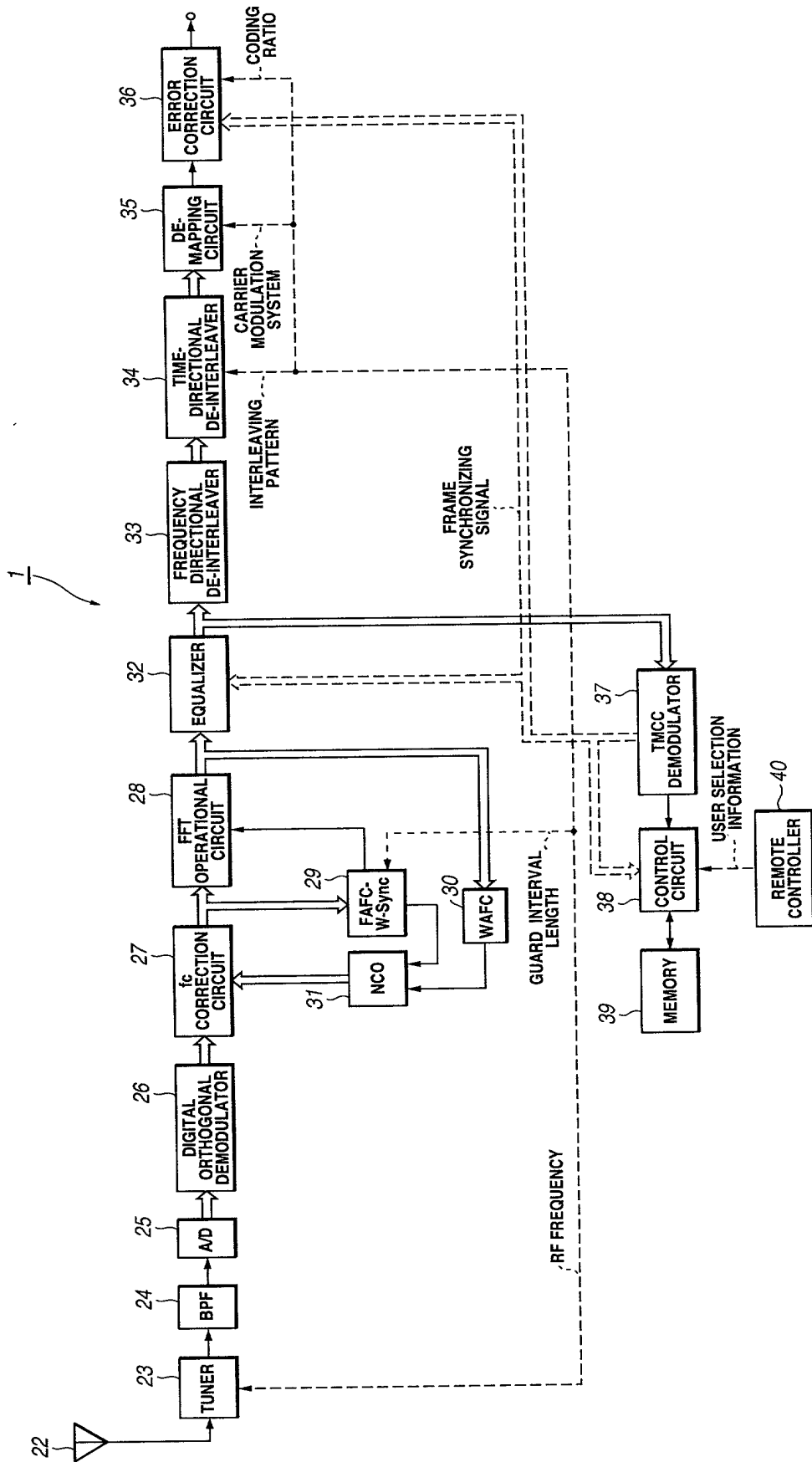


FIG. 16

BIT ASSIGNMENT	EXPLANATION
$B_{110} \sim B_{113}$	NUMBER OF CONNECTED SEGMENTS
$B_{114} \sim B_{117}$	SEGMENT NO. OF SIGNAL TO BE TRANSMITTED

FIG.17

VALUE ($b_{113}, b_{112}, b_{111}, b_{110}$)	MEANING
0000	RESERVED
0001	RESERVED
0010	2 SEGMENTS
0011	3 SEGMENTS
0100	4 SEGMENTS
.	.
1100	12 SEGMENTS
1101	13 SEGMENTS
1110	RESERVED
1111	INDEPENDENT TRANSMISSION

FIG.18

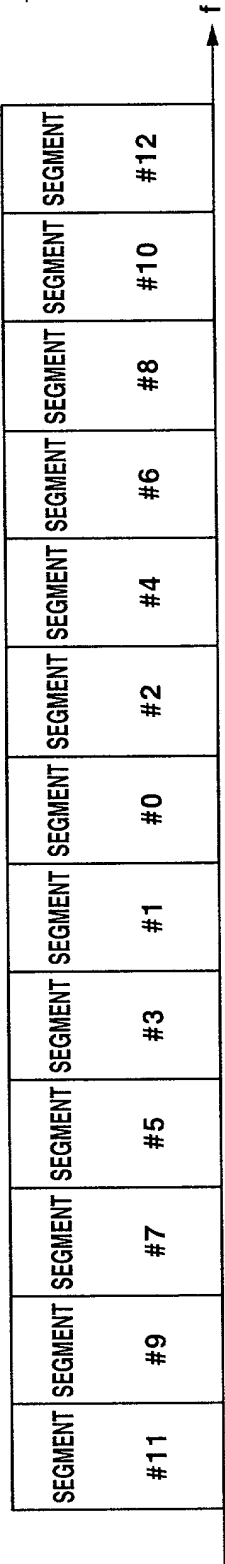


FIG.19

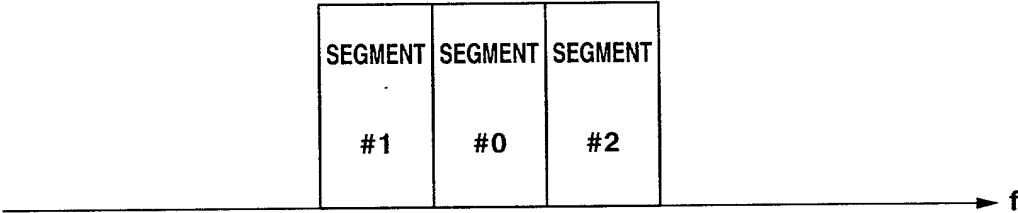


FIG.20

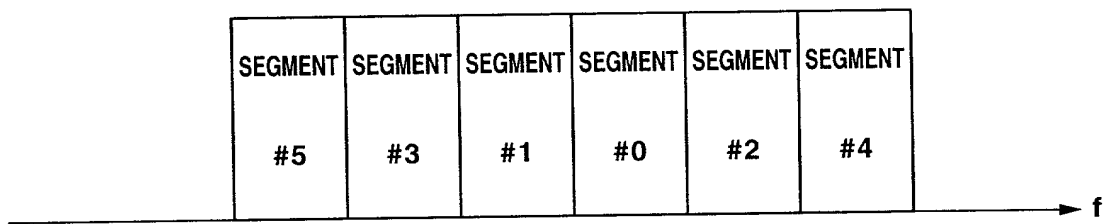


FIG.21

VALUE (b ₁₁₇ , b ₁₁₆ , b ₁₁₅ , b ₁₁₄)	MEANING
1111	SEGMENT #0
1110	SEGMENT #1
1101	SEGMENT #2
.	.
0011	SEGMENT #12
0010	RESERVED
0001	RESERVED
0000	RESERVED

FIG.22

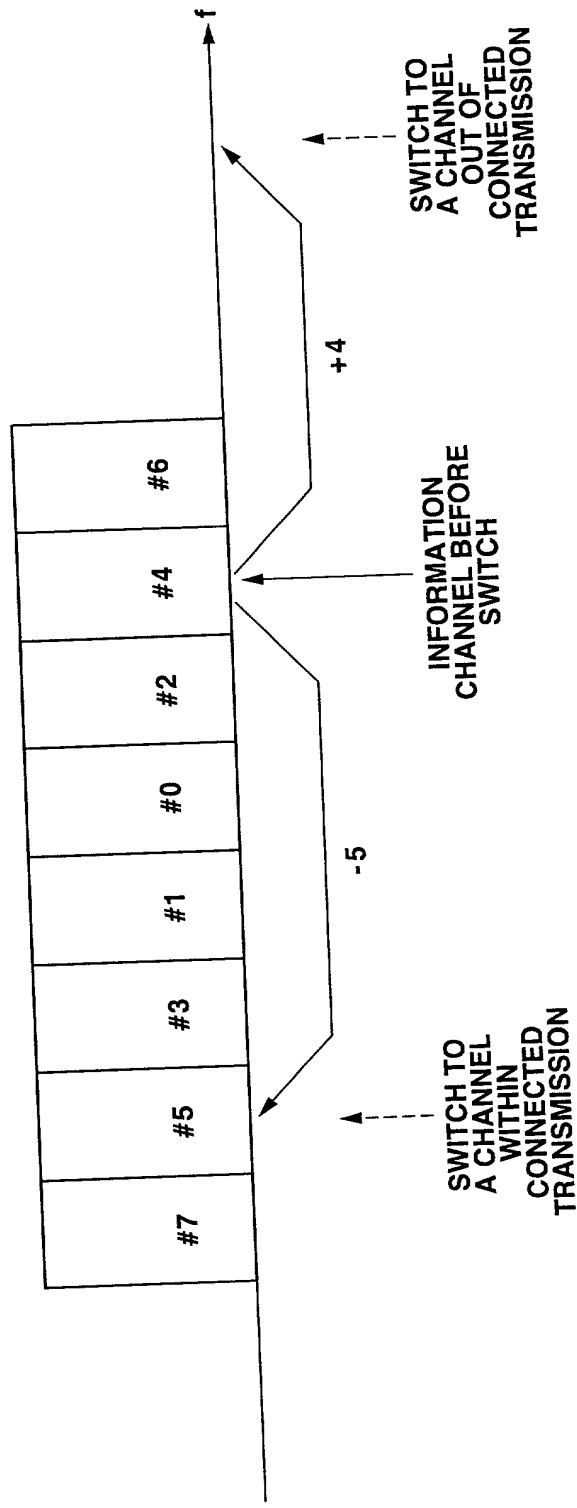


FIG.23

000	CONNECTED TRANSMISSION GROUP #0
001	CONNECTED TRANSMISSION GROUP #1
010	CONNECTED TRANSMISSION GROUP #2
011	CONNECTED TRANSMISSION GROUP #3
100	CONNECTED TRANSMISSION GROUP #4
101	CONNECTED TRANSMISSION GROUP #5
110	CONNECTED TRANSMISSION GROUP #6
111	INDEPENDENT TRANSMISSION

FIG.24

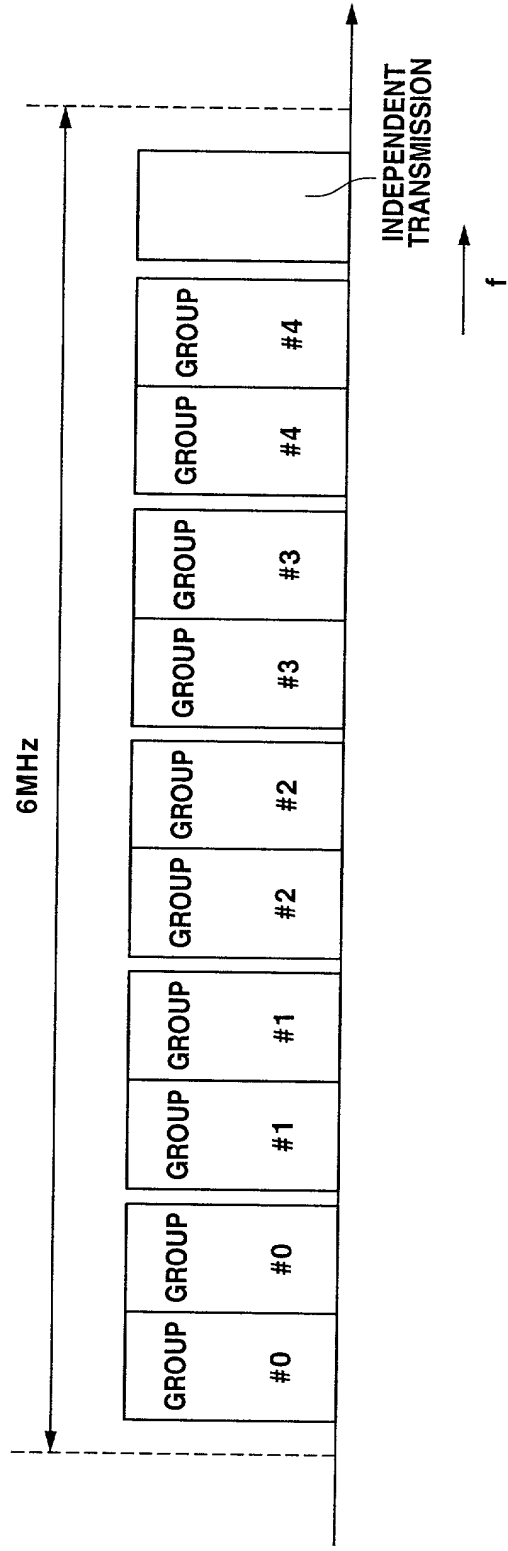


FIG.25